714 718
Class Subclass
ISSUE CLASSIFICATION

JC970 U.S. PTO
.09/871978
06/04/0

PATENT NUMBER

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U.S. REISSUE Patent Application

O.I.P.E.	O.G. PUBLICATION DATE	REISSUE PATENT DATE
FA SCANNED AG. 3 AA	8-14-01	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/871978	D	714	718	2133	Ton 7367

APPLICANTS
Makoto Hatakenaka
Akira Yamazaki
Shigeki Tomishima
Tadato Yamagata

TITLE
Semiconductor integrated circuit comprising synchronous DRAM core and logic circuit integrated into a single chip and method of testing the synchronous DRAM core

PTO-2040
12/99

☒ SURRENDER OF ORIGINAL PATENT _DT_ (Exr. initials)   ORIGINAL PATENT NUMBER _5,910,181_

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
714	718								
INTERNATIONAL CLASSIFICATION									
G 0 6 F	11/00								
							Continued on Issue Slip Inside File Jacket		

☐ TERMINAL DISCLAIMER

DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
15	15	5	14	10

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

(Assistant Examiner)   (Date)

☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____

☐ The terminal ____ months of this patent have been disclaimed.

DAVID TON
PRIMARY EXAMINER

David Ton   12/8/04
(Primary Examiner)   (Date)

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due	Date Paid

FINAL SPRE REVIEW
VM 9/16/05
(INITIALS)

(Legal Instruments Examiner)   (Date)

ISSUE BATCH NUMBER

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Form PTO-2003
(Rev. 6/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM
(Attached in pocket on right inside flap)

(FACE)

Issue Classification

Application No.	Applicant(s)
09/871,978	HATAKENAKA ET AL.
Examiner	**Art Unit**
David Ton	2133

ISSUE CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)

CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
714	718		

INTERNATIONAL CLASSIFICATION

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(Assistant Examiner)   (Date)

DAVID TON PRIMARY EXAMINER

Total Claims Allowed: 14

N. Jones 2·26·05
(Legal Instruments Examiner)   (Date)

David Ton 12/8/04
(Primary Examiner)   (Date)

O.G. Print Claim(s)	O.G. Print Fig
10	5

☐ Claims renumbered in the same order as presented by applicant   ☐ CPA   ☐ T.D.   ☐ R.1.47

Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original	Final	Original
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